

Sub C57

302. The receiver system of claim 284 further comprising:

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a channel estimation processor that receives via a particular one of said channel outputs, at least v frequency domain training symbols transmitted via a particular input to said channel, v being an extent in symbol periods of a duration of significant terms of an impulse response of channel component coupling said particular channel input and said particular channel, applies said time domain substantially orthogonalizing procedure to said received at least v frequency domain training symbols to obtain a time domain response for said channel component, and that applies an inverse of said substantially orthogonalizing procedure to a zero-padded version of said time domain response to obtain a frequency response for said channel component.

REMARKS

The present amendment is to address the Examiner's comments made over the telephone to the undersigned on April 17, 2000. It is believed that the amendments are fully responsive. Accordingly, reconsideration and allowance of all claims are respectfully requested. Please contact the undersigned at the telephone number given below if it would help expedite the prosecution of the present application in any way.

Respectfully submitted,

Dan Lang

Dan H. Lang
Reg. No. 38,531

4906 El Camino Real, Suite 205
Los Altos, CA 94022
Telephone: (650) 903-3500